

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YAMASHITA, Tomio**

Group Art Unit: 2811

Serial No.: 10/076,355

Examiner: VU, Hung K.

Filed: February 19, 2002

P.T.O. Confirmation No.: 1518

**FOR: SEMICONDUCTOR DEVICE INCLUDING AN ISLAND-LIKE DIELECTRIC MEMBER
EMBEDDED IN A CONDUCTIVE PATTERN (AS AMENDED)**

AMENDMENT TRANSMITTAL

Commissioner for Patents
Washington, D.C. 20231

April 17, 2003

Sir:

Transmitted herewith is an Amendment in the above-identified application.
The fee has been calculated as shown below:

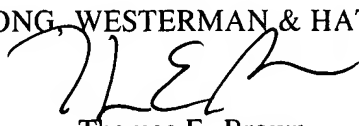
	CLAIMS AS AMENDED						
	Claims Remaining After Amendment	Highest Number Previously Paid For		Present Extra	Small Entity	Large Entity	Additional Fee
Total Claims	13	20	=	0	X \$9	X \$18	0.00
Independent Claims	5	4	=	1	X \$42	X \$84	84.00
First Presentation of Multiple Dependent Claims					\$140	280	0.00
TOTAL FEES ENCLOSED:							84.00

XX Enclosed please find our check in the amount of \$84.00 for the additional claims fee in connection with this amendment.

XX The Commissioner is hereby authorized to charge payment for any additional fees associated with this communication or credit any overpayment to Deposit Account No. 01-2340.

Respectfully submitted,

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

#7/a
4/23/3
under

In re the Application of: **YAMASHITA, Tomio**

Group Art Unit: 2811

Serial No.: 10/076,355

Examiner: VU, Hung K.

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P.T.O. Confirmation No.: 1518

For: **SEMICONDUCTOR DEVICE INCLUDING AN ISLAND-LIKE DIELECTRIC MEMBER EMBEDDED IN A CONDUCTIVE PATTERN (AS AMENDED)**

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents
Washington, D.C. 20231

April 17, 2003

Sir:

In response to the Office Action dated **January 17, 2003**, please amend the above-identified application as follows:

IN THE TITLE:

Please amend the title of the invention, in its entirety, so as to read as follows:

SEMICONDUCTOR DEVICE INCLUDING AN ISLAND-LIKE DIELECTRIC MEMBER EMBEDDED IN A CONDUCTIVE PATTERN.

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IN THE CLAIMS:

CANCEL claim 3.

AMEND claims 1 and 6 to read as follows:

1. (AMENDED) A semiconductor device having a multilayer wiring structure, comprising:
a semiconductor substrate;

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